

ABSTRACT OF THE DISCLOSURE

When a predetermined key is depressed in a power OFF state, status memory stores a status signal and a mask circuit outputs an ON signal. By the ON signal, a power circuit turns on the system power source. A processor starts to operate, accesses the status signal, and starts up a predetermined application program. When the predetermined key is depressed in a power ON state, the mask circuit masks the ON signal. A controller detects the operation of the predetermined key and outputs a signal for instructing the predetermined application program to be started up to the processor. When a power switch is depressed, the power circuit turns on the system power source. The processor starts to operate and accesses the status signal. Since no status signal is stored, the processor does not start up the predetermined application program.

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